深圳大经技术有限公司
Shenzhen Dajtech Co.,Ltd

CES-V210-CORE

Product Manual

ARM SOC Module

Rev. V1.0

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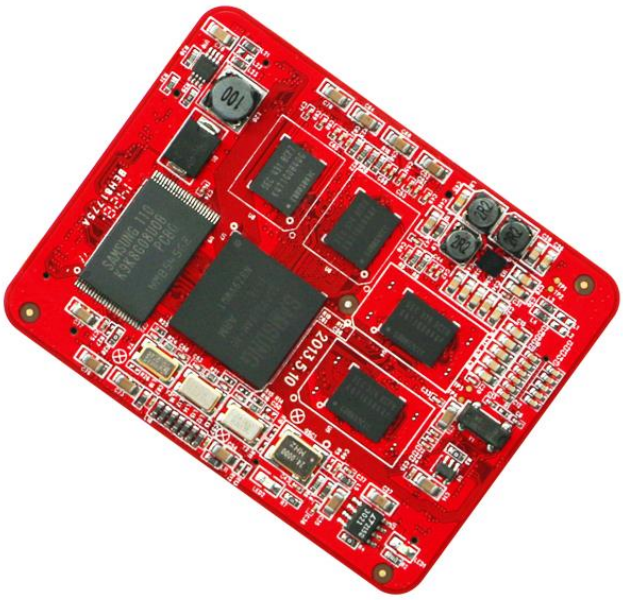

Introduction

CES-V210-CORE, based on Samsung ARM Cortex-A8 S5PV210 application processor, support 1GHz processing speed, onboard equipped an intelligent power management chip to achieve intelligent power management, with ultra-low power consumption and low temperature characteristics. Built-in 3D graphic engine, the processor provides superior image processing capabilities and HDMI high-definition video output interface.

CES-V210-CORE supports Windows CE, Android, Linux operating system. User can make any choice according to demand, and provide a complete solution for easier product rapid prototyping. CES-V210-CORE meets different product applications, including medical equipment, touch machine, industrial control equipment, self-service refueling equipment, handheld PDA, POS machine, teaching equipment and other industry products.

Features

- Adopt Samsung Cortex™-A8 S5PV210 Processor，frequency 1GHz
- Onboard 1GB DDR2，1GB NAND FLASH
- Support 2D/3D Image acceleration，support MPEG-4/ H.263/H.264 Format codec，up to 1080p@30fps
- Support 10M/100M Ethernet
- Support USB2.0、USB OTG2.0、UART、SD Card、GPIO、Camera
- Support Android4.0/2.3/2.2、Linux3.2 、Windows CE6.0/7.0
- Excellent Intelligent Power Management Circuit
- Excellent hardware design solutions, rapid product molding

Specification

Processor	
CPU	Samsung ARM Cortex-A8 S5PV210 processor, frequency 1GHz
Cache	32KB/32KB Cache and 512KB L2 Cache
Storage	
Memory	1GB DDR2
FLASH	1GB NAND FLASH
PMIC	
Chipset	MAX8698C
B2B Connector	
Connector Type	4* Molex 0.635mm double row 2 * 50PIN connector
PIN Number	400PIN
B2B Fixing Hole	
Fixing Hole	4
Operating System	
Operating System	Optional Android4.0/2.3/2.2、Linux3.2、WinCE6.0/7.0
Environment	
Working Environment	Temperature：-10~60℃，Humidity：5%~95%RH@31℃ no condensation
Storage Environment	Temperature：-40~85℃，Humidity：5%~95%RH@39℃ no condensation
Size	
Size(mm)	76.5*58.9mm

Pin Definition

J1				
PIN NO.	Signal Name	CPU Ball Number	CPU Ball Name	Remark
1	XCLKOUT	AE24	XCLKOUT	XCLKOUT
2	GND			Power Ground
3	XI2CSDA1	AE23	XI2C1SDA	I2C
4	XI2CSCL1	AD22	XI2C1SCL	I2C
5	XI2CSDA2/IEM_SCLK	AC16	XI2C2SDA	I2C
6	XI2CSCL2/IEM_SPWI	AE22	XI2C2SCL	I2C
7	GND			Power Ground
8	GND			Power Ground
9	XUHDP	AE19	XUHOSTDP	USB
10	XUHDN	AD19	XUHOSTDM	USB
11	XUHREXT	AC17	XUHOSTREXT	USB
12	XUHPWREN	AD23	XUHOSTPWREN	USB
13	XUHOVERCUR	AC22	XUHOSTOVERCUR	USB
14	GND			Power Ground
15	GND			Power Ground
16	GND			Power Ground
17	XUODP	AD21	XUOTGDP	USB
18	XUODM	AE21	XUOTGDM	USB
19	XUOREXT	AE18	XUOTGREXT	USB
20	XUOID	AD18	XUOTGID	USB
21	XUOVBUS	AC18	XUOTGVBUS	USB
22	XUODRVVBUS	AC19	XUOTGDRVVBUS	USB
23	GND			Power Ground
24	GND			Power Ground
25	XEINT0/PSHOLD	Y21	XEINT_0	INT/GPIO
26	XEINT1	W25	XEINT_1	INT/GPIO
27	XEINT2	W23	XEINT_2	INT/GPIO
28	XEINT3	Y25	XEINT_3	INT/GPIO
29	XEINT4	AA22	XEINT_4	INT/GPIO
30	XEINT5	W24	XEINT_5	INT/GPIO
31	XEINT6	W21	XEINT_6	INT/GPIO
32	XEINT7	AA25	XEINT_7	INT/GPIO
33	XEINT8	V20	XEINT_8	INT/GPIO
34	XEINT9	V22	XEINT_9	INT/GPIO

35	XEINT10	Y24	XEINT_10	INT/GPIO
36	XEINT11	W22	XEINT_11	INT/GPIO
37	XEINT12/HDMI_CEC	AA24	XEINT_12	INT/GPIO
38	XEINT13/HDMI_HPD	AC23	XEINT_13	INT/GPIO
39	XEINT14	AB25	XEINT_14	INT/GPIO
40	XEINT15	W20	XEINT_15	INT/GPIO
41	XEINT16/KP_COL0	U20	XEINT_16	INT/GPIO
42	XEINT17/KP_COL1	Y23	XEINT_17	INT/GPIO
43	XEINT18/KP_COL2	V21	XEINT_18	INT/GPIO
44	XEINT19/KP_COL3	AB24	XEINT_19	INT/GPIO
45	XEINT20/KP_COL4	AA21	XEINT_20	INT/GPIO
46	XEINT21/KP_COL5	AA23	XEINT_21	INT/GPIO
47	XEINT22/KP_COL6	AC25	XEINT_22	INT/GPIO
48	XEINT23/KP_COL7	Y20	XEINT_23	INT/GPIO
49	XEINT24/KP_ROW0	AC24	XEINT_24	INT/GPIO
50	XEINT25/KP_ROW1	AB22	XEINT_25	INT/GPIO
51	XEINT26/KP_ROW2	AD25	XEINT_26	INT/GPIO
52	XEINT27/KP_ROW3	Y22	XEINT_27	INT/GPIO
53	XEINT28/KP_ROW4	AD24	XEINT_28	INT/GPIO
54	XEINT29/KP_ROW5	AA20	XEINT_29	INT/GPIO
55	XEINT30/KP_ROW6	Y19	XEINT_30	INT/GPIO
56	XEINT31/KP_ROW7	AB23	XEINT_31	INT/GPIO
57	XOM0	T23	XOM_0	Boot Mode
58	XOM1	T22	XOM_1	Boot Mode
59	XOM2	V23	XOM_2	Boot Mode
60	XOM3	U21	XOM_3	Boot Mode
61	XOM4	V25	XOM_4	Boot Mode
62	XOM5	V24	XOM_5	Boot Mode
63	XNWRESET	T21	XNWRESET	XNWRESET
64	XNRESET	U23	XNRESET	XNRESET
65	XNRSTOUT	T20	XNRSTOUT	XNRSTOUT
66	NC			NC
67	NC			NC
68	NC			NC
69	NC			NC
70	JIG_ON			Reserve
71	PMIC_NRST_OUT			PMU Reset output
72	POWER_KEY			PMU Switch control（input）
73	PWRON			PMU Control HOLD(input)
74	NC			NC

75	PWRON_BOARD			PMU Control HOLD(input)
76	PWRRGTON_INV			PWRRGTON_INV
77	VDD_RTC			RTC Power
78	GND			Power Ground
79	PVDD_LDO4			PMU Power Output LDO4
80	PVDD_LDO4			PMU Power Output LDO4
81	PVDD_LDO5			PMU Power Output LDO5
82	PVDD_LDO5			PMU Power Output LDO5
83	PVDD_LDO6			PMU Power Output LDO6
84	PVDD_LDO6			PMU Power Output LDO6
85	PVDD_LDO7			PMU Power Output LDO7
86	PVDD_LDO7			PMU Power Output LDO7
87	VDD_CAM_IO_2V8			CAMERA Power（input）
88	VDD_CAM_IO_2V8			CAMERA Power（input）
89	PVDD_MEM			PMU Power Output (Reserved)
90	PVDD_MEM			PMU Power Output (Reserved)
91	VDD_DAC_AP			DAC Power（Output）
92	VDD_DAC_AP			DAC Power（Output）
93	PVDD_LDO9			PMU Power Output LDO9
94	PVDD_LDO9			PMU Power Output LDO9
95	VDD_3V3_CORE			DC 3.3V(input)
96	VDD_3V3_CORE			DC 3.3V(input)
97	VDD_3V3			DC 3.3V(input)
98	VDD_3V3			DC 3.3V(input)
99	DC5V			DC 5V(input)
100	DC5V			DC 5V(input)

J2				
PIN NO.	Signal Name	CPU Ball Number	CPU Ball Name	Remark
1	XSPICLK1	G12	XSPICLK_1	SPI
2	XSPICSN1	B11	XSPICSN_1	SPI
3	XSPIMISO1	G13	XSPIMISO_1	SPI
4	XSPIMOSI1	A11	XSPIMOSI_1	SPI
5	GND			Power Ground
6	GND			Power Ground
7	XPWMTOUT0	E8	XPWMTOUT_0	PWM
8	XPWMTOUT1	B9	XPWMTOUT_1	PWM

9	XPWMTOUT2	A8	XPWMTOUT_2	PWM
10	XPWMTOUT3/PWM_MIE	F12	XPWMTOUT_3	PWM
11	GND			Power Ground
12	GND			Power Ground
13	XMMC3CLK	A9	XMMC3CLK	SD/MMC
14	XMMC3CMD	D10	XMMC3CMD	SD/MMC
15	XMMC3CDN	E11	XMMC3CDN	SD/MMC
16	XMMC3DATA0/MMC2_DATA4	B10	XMMC3DATA_0	SD/MMC
17	XMMC3DATA1/MMC2_DATA5	C10	XMMC3DATA_1	SD/MMC
18	XMMC3DATA2/MMC2_DATA6	D11	XMMC3DATA_2	SD/MMC
19	XMMC3DATA3/MMC2_DATA7	A10	XMMC3DATA_3	SD/MMC
20	GND			Power Ground
21	GND			Power Ground
22	GND			Power Ground
23	XI2CSDA0	F11	XI2C0SDA	I2C
24	XI2CSCL0	C9	XI2C0SCL	I2C
25	NC			NC
26	GND			Power Ground
27	XURXD0	C8	XURXD_0	UART
28	XUTXD0	D8	XUTXD_0	UART
29	XUCTSN0	D9	XUCTSN_0	UART
30	XURTSN0	A7	XURTSN_0	UART
31	XURXD1	G10	XURXD_1	UART
32	XUTXD1	F10	XUTXD_1	UART
33	XUCTSN1	B8	XUCTSN_1	UART
34	XURTSN1	E10	XURTSN_1	UART
35	GND			Power Ground
36	GND			Power Ground
37	XSPICLK0	B7	XSPICLK_0	SPI
38	XSPICSN0	E9	XSPICSN_0	SPI
39	XSPIMISO0	J9	XSPIMISO_0	SPI
40	XSPIMOSI0	J11	XSPIMOSI_0	SPI
41	GND			Power Ground
42	GND			Power Ground
43	XMMC0CLK	B5	XMMC0CLK	SD/MMC
44	XMMC0CMD	E6	XMMC0CMD	SD/MMC
45	XMMC0CDN	F7	XMMC0CDN	SD/MMC

46	XMMC0DATA0	C5	XMMC0DATA_0	SD/MMC
47	XMMC0DATA1	A5	XMMC0DATA_1	SD/MMC
48	XMMC0DATA2	D6	XMMC0DATA_2	SD/MMC
49	XMMC0DATA3	C6	XMMC0DATA_3	SD/MMC
50	GND			Power Ground
51	XMMC1CLK	B6	XMMC1CLK	SD/MMC
52	XMMC1CMD	F8	XMMC1CMD	SD/MMC
53	XMMC1CDN	C7	XMMC1CDN	SD/MMC
54	XMMC1DATA0/MMC 0_DATA4	D7	XMMC1DATA_0	SD/MMC
55	XMMC1DATA1/MMC 0_DATA5	E7	XMMC1DATA_1	SD/MMC
56	XMMC1DATA2/MMC 0_DATA6	A6	XMMC1DATA_2	SD/MMC
57	XMMC1DATA3/MMC 0_DATA7	F9	XMMC1DATA_3	SD/MMC
58	GND			Power Ground
59	GND			Power Ground
60	GND			Power Ground
61	XMSMADDR0/CAM_ B_D0/CF_ADDR0/MIP I_BYTE_CLK	H1	XMSMADDR_0	MODEM
62	XMSMADDR1/CAM_ B_D1/CF_ADDR1/MIP I_ESC_CLK	G6	XMSMADDR_1	MODEM
63	XMSMADDR2/CAM_ B_D2/CF_ADDR2/TS_ CLK	E4	XMSMADDR_2	MODEM
64	XMSMADDR3/CAM_ B_D3/CF_IORDY/TS_ SYNC	H7	XMSMADDR_3	MODEM
65	XMSMADDR4/CAM_ B_D4/CF_INTRQ/TS_ VAL	G1	XMSMADDR_4	MODEM
66	XMSMADDR5/CAM_ B_D5/CF_DMARQ/TS _DATA	H2	XMSMADDR_5	MODEM
67	XMSMADDR6/CAM_ B_D6/CF_DRESETN/T S_ERROR	F5	XMSMADDR_6	MODEM
68	XMSMADDR7/CAM_ B_D7/CF_DMACKN/ MHL_D0	D5	XMSMADDR_7	MODEM
69	XMSMADDR8/CAM_	F6	XMSMADDR_8	MODEM

	B_PCLK/SROM_ADDR16/MHL_D1			
70	XMSMADDR9/CAM_B_VSYNC/SROM_ADDR17/MHL_D2	G2	XMSMADDR_9	MODEM
71	XMSMADDR10/CAM_B_HREF/SROM_ADDR18/MHL_D3	F1	XMSMADDR_10	MODEM
72	XMSMADDR11/CAM_B_FIELD/SROM_ADDR19/MHL_D4	G3	XMSMADDR_11	MODEM
73	XMSMADDR12/CAM_B_CLKOUT/SROM_ADDR20/MHL_D5	E5	XMSMADDR_12	MODEM
74	XMSMADDR13/KP_COL0/SROM_ADDR21/MHL_D6	F2	XMSMADDR_13	
75	GND			Power Ground
76	GND			Power Ground
77	XMSMDATA0/KP_COL1/CF_DATA0/MHL_D7	F3	XMSMDATA_0	MODEM
78	XMSMDATA1/KP_COL2/CF_DATA1/MHL_D8	E2	XMSMDATA_1	MODEM
79	XMSMDATA2/KP_COL3/CF_DATA2/MHL_D9	E1	XMSMDATA_2	MODEM
80	XMSMDATA3/KP_COL4/CF_DATA3/MHL_D10	D3	XMSMDATA_3	MODEM
81	XMSMDATA4/KP_COL5/CF_DATA4/MHL_D11	D1	XMSMDATA_4	MODEM
82	XMSMDATA5/KP_COL6/CF_DATA5/MHL_D12	E3	XMSMDATA_5	MODEM
83	XMSMDATA6/KP_COL7/CF_DATA6/MHL_D13	D2	XMSMDATA_6	MODEM
84	XMSMDATA7/KP_ROW0/CF_DATA7/MHL_D14	C1	XMSMDATA_7	MODEM
85	XMSMDATA8/KP_ROW1/CF_DATA8/MHL_	C2	XMSMDATA_8	MODEM

	D15			
86	XMSMDATA9/KP_ROW2/CF_DATA9/MHL_D16	D4	XMSMDATA_9	MODEM
87	XMSMDATA10/KP_ROW3/CF_DATA10/MHL_D17	B1	XMSMDATA_10	MODEM
88	XMSMDATA11/KP_ROW4/CF_DATA11/MHL_D18	C3	XMSMDATA_11	MODEM
89	XMSMDATA12/KP_ROW5/CF_DATA12/MHL_D19	C4	XMSMDATA_12	MODEM
90	XMSMDATA13/KP_ROW6/CF_DATA13/MHL_D20	B2	XMSMDATA_13	MODEM
91	XMSMDATA14/KP_ROW7/CF_DATA14/MHL_D21	B3	XMSMDATA_14	MODEM
92	XMSMDATA15/KP_ROW8/CF_DATA15/MHL_D22	A2	XMSMDATA_15	MODEM
93	GND			Power Ground
94	GND			Power Ground
95	XMSMCSN/KP_ROW9/CF_CSN0/MHL_D23	G8	XMSMCSN	MODEM
96	XMSMWEN/KP_ROW10/CF_CSN1/MHL_HSYNC	B4	XMSMWEN	MODEM
97	XMSMREN/KP_ROW11/CF_IORN/MHL_IDCK	G9	XMSMRN	MODEM
98	XMSMIRQN/KP_ROW12/CF_IOWN/MHL_VSYNC	A3	XMSMIRQN	MODEM
99	XMSMADVN/KP_ROW13/SROM_ADDR22/MHL_DE	A4	XMSMADVN	MODEM
100	GND			Power Ground

J3				
PIN NO.	Signal Name	CPU Ball Number	CPU Ball Name	Remark
1	XI2SSCLK0/PCM_SCLK2	AD2	XI2S0SCLK	AUDIO
2	XI2SCDCLK0/PCM_EXTCLK2	AC4	XI2S0CDCLK	AUDIO
3	XI2SLRCK0/PCM_FSYNC2	AE3	XI2S0LRCK	AUDIO
4	XI2SSDIO/PCM_SIN2	AE2	XI2S0SDI	AUDIO
5	XI2SSDO0_0/PCM_SOUT2	AD3	XI2S0SDO_0	AUDIO
6	XI2SSDO0_1	AC3	XI2S0SDO_1	AUDIO
7	XI2SSDO0_2	AA3	XI2S0SDO_2	AUDIO
8	GND			Power Ground
9	GND			Power Ground
10	GND			Power Ground
11	XVHSYNC/SYSCS0/VENHSYNC	AA13	XVHSYNC	LCD
12	XVVSYNC/SYSCS1/VENVSYNC	Y10	XVVSYNC	LCD
13	XVVDEN/SYSRC/VENHREF	AB10	XVVDEN	LCD
14	XVVCLK/SYSWE/V601CLK	AA10	XVVCLK	LCD
15	XVVD0	AA9	XVVD_0	LCD
16	XVVD1	AB9	XVVD_1	LCD
17	XVVD2	AB8	XVVD_2	LCD
18	XVVD3	AB7	XVVD_3	LCD
19	XVVD4	Y9	XVVD_4	LCD
20	XVVD5	AB6	XVVD_5	LCD
21	XVVD6	AE7	XVVD_6	LCD
22	XVVD7	AC9	XVVD_7	LCD
23	XVVD8	AA8	XVVD_8	LCD
24	XVVD9	W9	XVVD_9	LCD
25	XVVD10	AE6	XVVD_10	LCD
26	XVVD11	AC8	XVVD_11	LCD
27	XVVD12	Y8	XVVD_12	LCD
28	XVVD13	AC7	XVVD_13	LCD
29	XVVD14	AD6	XVVD_14	LCD
30	XVVD15	AE5	XVVD_15	LCD
31	XVVD16	AD7	XVVD_16	LCD

32	XVVD17	AA7	XVVD_17	LCD
33	XVVD18	AD5	XVVD_18	LCD
34	XVVD19	AA6	XVVD_19	LCD
35	XVVD20	AB5	XVVD_20	LCD
36	XVVD21	AC5	XVVD_21	LCD
37	XVVD22	AC6	XVVD_22	LCD
38	XVVD23	Y7	XVVD_23	LCD
39	VSYNC_LDI	W8	XVVSYNC_LDI	LCD
40	SYS_OE/VEN_FIELD	AE4	XVSYS_OE	LCD
41	GND			Power Ground
42	GND			Power Ground
43	XMIPISDPCLK	AD10	XMIPISDPCLK	MIPI DPHY
44	XMIPISDNCLK	AE10	XMIPISDNCLK	MIPI DPHY
45	XMIPISDP0	AD12	XMIPISDP0	MIPI DPHY
46	XMIPISDN0	AE12	XMIPISDN0	MIPI DPHY
47	XMIPISDP1	AD11	XMIPISDP1	MIPI DPHY
48	XMIPISDN1	AE11	XMIPISDN1	MIPI DPHY
49	XMIPISDP2	AD9	XMIPISDP2	MIPI DPHY
50	XMIPISDN2	AE9	XMIPISDN2	MIPI DPHY
51	XMIPISDP3	AD8	XMIPISDP3	MIPI DPHY
52	XMIPISDN3	AE8	XMIPISDN3	MIPI DPHY
53	GND			Power Ground
54	GND			Power Ground
55	XADCAIN0	AC11	XADCAIN_0	ADC
56	XADCAIN1	AC12	XADCAIN_1	ADC
57	XADCAIN2	AB11	XADCAIN_2	ADC
58	XADCAIN3	AC10	XADCAIN_3	ADC
59	XADCAIN4	Y11	XADCAIN_4	ADC
60	XADCAIN5	W12	XADCAIN_5	ADC
61	XADCAIN6	Y12	XADCAIN_6	ADC
62	XADCAIN7	AA12	XADCAIN_7	ADC
63	XADCAIN8	AA11	XADCAIN_8	ADC
64	XADCAIN9	AB12	XADCAIN_9	ADC
65	GND			Power Ground
66	GND			Power Ground
67	GND			Power Ground
68	GND			Power Ground
69	XMIPIMDPCLK	AE15	XMIPIMDPCLK	MIPI DPHY
70	XMIPIMDNCLK	AD15	XMIPIMDNCLK	MIPI DPHY
71	XMIPIMDP0	AE17	XMIPIMDP0	MIPI DPHY
72	XMIPIMDN0	AD17	XMIPIMDN0	MIPI DPHY

73	XMIPIMDP1	AE16	XMIPIMDP1	MIPI DPHY
74	XMIPIMDN1	AD16	XMIPIMDN1	MIPI DPHY
75	XMIPIMDP2	AE14	XMIPIMDP2	MIPI DPHY
76	XMIPIMDN2	AD14	XMIPIMDN2	MIPI DPHY
77	XMIPIMDP3	AE13	XMIPIMDP3	MIPI DPHY
78	XMIPIMDN3	AD13	XMIPIMDN3	MIPI DPHY
79	GND			Power Ground
80	GND			Power Ground
81	XURXD2/UART_AUDIO_RXD	AC20	XURXD_2	UART
82	XUTXD2/UART_AUDIO_TXD	AC14	XUTXD_2	UART
83	XURXD3/CTSN2/UART_AUDIO_CTSN	AC13	XURXD_3	UART
84	XUTXD3/RTSN2/UART_AUDIO_RTSN	AB13	XUTXD_3	UART
85	GND			Power Ground
86	GND			Power Ground
87	XCIPCLK	AC21	XCIPCLK	CAMERA
88	XCIVSYNC	AA14	XCIVSYNC	CAMERA
89	XCIHREF	AB14	XCIHREF	CAMERA
90	XCIYDATA0	AB15	XCIDATA_0	CAMERA
91	XCIYDATA1	AB16	XCIDATA_1	CAMERA
92	XCIYDATA2	AB20	XCIDATA_2	CAMERA
93	XCIYDATA3	AA19	XCIDATA_3	CAMERA
94	XCIYDATA4	AB21	XCIDATA_4	CAMERA
95	XCIYDATA5	Y18	XCIDATA_5	CAMERA
96	XCIYDATA6	AB17	XCIDATA_6	CAMERA
97	XCIYDATA7	AA17	XCIDATA_7	CAMERA
98	CSI_CLK	AA18	XCICLKENB	CAMERA
99	CAM_A_CLK	AA18	XCICLKENB	CAMERA
100	XCIFIELD	AB19	XCIFIELD	CAMERA

J4				
PIN NO.	Signal Name	CPU Ball Number	CPU Ball Name	Remark
1	XM0ADDR0	K5	XM0ADDR_0	OneNAND(EBI)
2	XM0ADDR1	L7	XM0ADDR_1	OneNAND(EBI)
3	XM0ADDR2	J4	XM0ADDR_2	OneNAND(EBI)
4	XM0ADDR3	H5	XM0ADDR_3	OneNAND(EBI)
5	XM0ADDR4	J6	XM0ADDR_4	OneNAND(EBI)
6	XM0ADDR5	K4	XM0ADDR_5	OneNAND(EBI)
7	XM0ADDR6	K6	XM0ADDR_6	OneNAND(EBI)
8	XM0ADDR7	J5	XM0ADDR_7	OneNAND(EBI)
9	XM0ADDR8	H4	XM0ADDR_8	OneNAND(EBI)
10	XM0ADDR9	G4	XM0ADDR_9	OneNAND(EBI)
11	XM0ADDR10	J3	XM0ADDR_10	OneNAND(EBI)
12	XM0ADDR11	K7	XM0ADDR_11	OneNAND(EBI)
13	XM0ADDR12	H6	XM0ADDR_12	OneNAND(EBI)
14	XM0ADDR13	G5	XM0ADDR_13	OneNAND(EBI)
15	XM0ADDR14	F4	XM0ADDR_14	OneNAND(EBI)
16	XM0ADDR15	H3	XM0ADDR_15	OneNAND(EBI)
17	GND			Power Ground
18	GND			Power Ground
19	XM0DATA0	K3	XM0DATA_0	OneNAND(EBI)
20	XM0DATA1	L3	XM0DATA_1	OneNAND(EBI)
21	XM0DATA2	L5	XM0DATA_2	OneNAND(EBI)
22	XM0DATA3	M4	XM0DATA_3	OneNAND(EBI)
23	XM0DATA4	N1	XM0DATA_4	OneNAND(EBI)
24	XM0DATA5	N2	XM0DATA_5	OneNAND(EBI)
25	XM0DATA6	P1	XM0DATA_6	OneNAND(EBI)
26	XM0DATA7	N4	XM0DATA_7	OneNAND(EBI)
27	XM0DATA8	L1	XM0DATA_8	OneNAND(EBI)
28	XM0DATA9	L2	XM0DATA_9	OneNAND(EBI)
29	XM0DATA10	L4	XM0DATA_10	OneNAND(EBI)
30	XM0DATA11	M1	XM0DATA_11	OneNAND(EBI)
31	XM0DATA12	M3	XM0DATA_12	OneNAND(EBI)
32	XM0DATA13	M5	XM0DATA_13	OneNAND(EBI)
33	XM0DATA14	N5	XM0DATA_14	OneNAND(EBI)
34	XM0DATA15	P2	XM0DATA_15	OneNAND(EBI)
35	GND			Power Ground
36	GND			Power Ground
37	XM0CSN0	U3	XM0CSN_0	OneNAND(EBI)

38	XM0CSN1	T4	XM0CSN_1	OneNAND(EBI)
39	XM0CSN2/NFCSN0	J1	XM0CSN_2	OneNAND(EBI)
40	XM0CSN3/NFCSN1	N9	XM0CSN_3	OneNAND(EBI)
41	XM0CSN4/NFCSN2/ONANDXL_CSN0	N3	XM0CSN_4	OneNAND(EBI)
42	XM0CSN5/NFCSN3/ONANDXL_CSN1	N7	XM0CSN_5	OneNAND(EBI)
43	XM0OEN	R4	XM0OEN	OneNAND(EBI)
44	XM0WEN	P4	XM0WEN	OneNAND(EBI)
45	XM0BE0	T3	XM0BEN_0	OneNAND(EBI)
46	XM0BE1	N6	XM0BEN_1	OneNAND(EBI)
47	XM0WAITN	W2	XM0WAITN	OneNAND(EBI)
48	XM0DATA_RDN	M7	XM0DATA_RDN	OneNAND(EBI)
49	XM0FCLE/ONDXL_AVD	K1	XM0FCLE	OneNAND(EBI)
50	XM0FALE/ONDXL_SMCLK	K2	XM0FALE	OneNAND(EBI)
51	XM0FWEN/ONDXL_RPN	J2	XM0FWEN	OneNAND(EBI)
52	XM0FREN	M2	XM0FREN	OneNAND(EBI)
53	XM0FRNB0/ONDXL_INT0	R3	XM0FRNB_0	OneNAND(EBI)
54	XM0FRNB1/ONDXL_INT1	M6	XM0FRNB_1	OneNAND(EBI)
55	XM0FRNB2	V3	XM0FRNB_2	OneNAND(EBI)
56	XM0FRNB3	L6	XM0FRNB_3	OneNAND(EBI)
57	GND			Power Ground
58	GND			Power Ground
59	XHDMITX0P	T2	XHDMITX0P	HDMI
60	XHDMITX0N	T1	XHDMITX0N	HDMI
61	XHDMITX1P	U2	XHDMITX1P	HDMI
62	XHDMITX1N	U1	XHDMITX1N	HDMI
63	XHDMITX2P	V2	XHDMITX2P	HDMI
64	XHDMITX2N	V1	XHDMITX2N	HDMI
65	XHDMITXCP	R2	XHDMITXCP	HDMI
66	XHDMITXCN	R1	XHDMITXCN	HDMI
67	XHDMIREXT	W1	XHDMIREXT	HDMI
68	GND			Power Ground
69	GND			Power Ground
70	GND			Power Ground
71	XJTRSTN	P5	XJTRSTN	JTAG
72	XJTMS	R5	XJTMS	JTAG

73	XJTCK	U4	XJTCK	JTAG
74	XJTDI	T5	XJTDI	JTAG
75	XJTDO	W3	XJTDO	JTAG
76	GND			Power Ground
77	GND			Power Ground
78	GND			Power Ground
79	XMMC2CLK/SPI_2_CLK	Y6	XMMC2CLK	SD/MMC
80	XMMC2CMD/SPI_2_NSS	W6	XMMC2CMD	SD/MMC
81	XMMC2CDN/SPI_2_MISO	AA4	XMMC2CDN	SD/MMC
82	XMMC2DATA0/SPI_2_MOSI	Y4	XMMC2DATA_0	SD/MMC
83	XMMC2DATA1	Y5	XMMC2DATA_1	SD/MMC
84	XMMC2DATA2	Y3	XMMC2DATA_2	SD/MMC
85	XMMC2DATA3	W4	XMMC2DATA_3	SD/MMC
86	GND			Power Ground
87	GND			Power Ground
88	DACOUT0	U5	XDACOUT	DAC
89	XI2SSCLK1/PCM_SCLK1/AC97_BITCLK	AD1	XI2S1SCLK	AUDIO
90	GND			Power Ground
91	XI2SLRCK1/PCM_FSYNC1/AC97_SYNC	AC2	XI2S1LRCK	AUDIO
92	XI2SCDCLK1/PCM_EXTCLK1/AC97_RESETN	AB3	XI2S1CDCLK	AUDIO
93	XI2SSDO1/PCM_SOUT1/AC97_SDO	AB4	XI2S1SDO	AUDIO
94	XI2SSDI1/PCM_SIN1/AC97_SDI	AA5	XI2S1SDI	AUDIO
95	XPCMSCLK0/SPDIF_OUT0/XI2SSCLK2	AA2	XPCM0SCLK	AUDIO
96	GND			Power Ground
97	XPCMFSYNC0/LCD_FRM/XI2SLRCK2	AB1	XPCM0FSYNC	AUDIO
98	XPCMEXTCLK0/SPDIF_EXTCLK/XI2SCDCLK2	AA1	XPCM0EXTCLK	AUDIO
99	XPCMSOUT0/XI2SSDO2	AC1	XPCM0SOUT	AUDIO
100	XPCMSIN0/XI2SSDI2	AB2	XPCM0SIN	AUDIO

Service Support

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E-mail：ces_support@ces-tech.com

Technical Support Service Hours：

Monday to Friday：9：00～12：00，13：30～18：00

Statement

The information in this manual is for reference only and is subject to change without notice.

For more product information, visit www.nrisc.cn

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